

FIG. 1

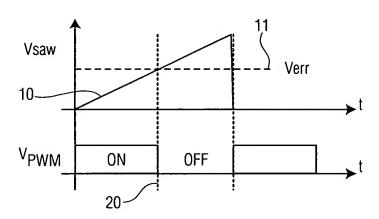


FIG. 2

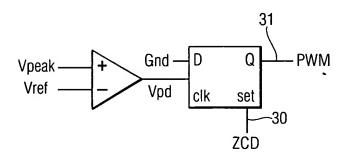
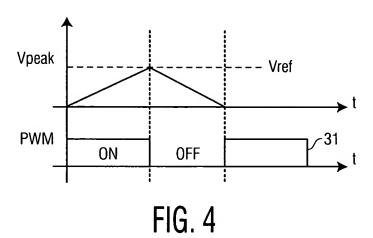


FIG. 3



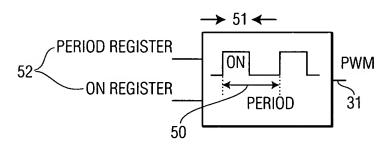


FIG. 5

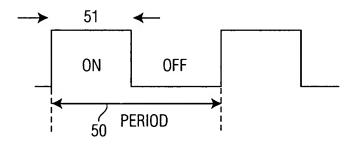


FIG. 6

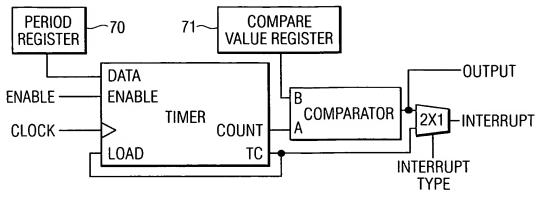
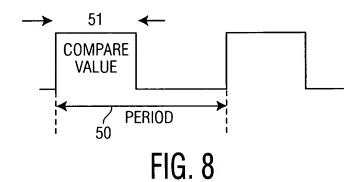


FIG. 7 PRIOR ART



Min_on register

Max_on register

Min_off register

Max_off register

Force_off event

Force_on event

Min_off register

90

93

PWM

31

FIG. 9

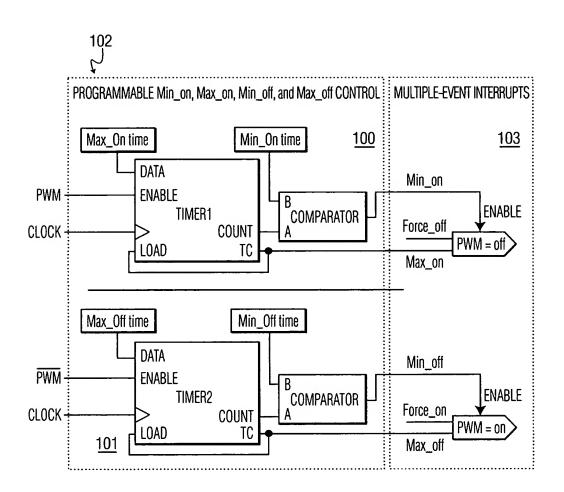
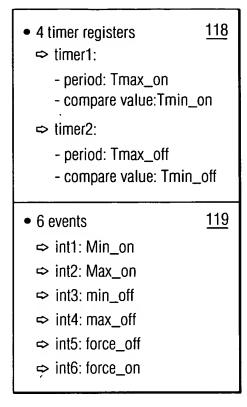


FIG. 10



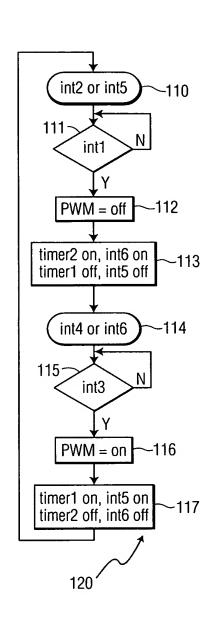


FIG. 11